

ABSTRACT OF THE DISCLOSURE

A system and method are described for receiving differential currents in a current mode circuit. When condition occur where the receiver inputs are floating, undriven, shorted together, or one or both shorted to ground, the output of the system remains stable. Diode connected MOS transistors receive the unequal currents, and current mirrors
5 amplify the received currents. Those amplified mirrored currents are differentially amplified and converted into voltage signals suitable of typical computer and logic systems. The current mode differential nature of the invention provides high common mode current and voltage noise immunity. A threshold for the unequal currents helps provide high
10 differential current and voltage noise immunity.